



Aprisa XE Software Release Notes

8.2.10b

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4RF Communications Ltd

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1. Introduction

Introduction

The previous Aprisa XE software version release relevant to this release is:

RF Variant	Software version	Release date
All	8.1.50	29 th February 2008

This release of Aprisa XE software is:

RF Variant	Software version	Release date
All	8.2.10	18 th June 2008

This document covers the major changes, product enhancements, new functionality, bug fixes and MIB changes since Aprisa XE software version 8.1.50.

4RF Support

Prior to upgrading Aprisa XE terminals with this software, please contact 4RF Customer Support at support@4rf.com to obtain the upgrade files and upgrade process.

Where possible, the customer should consider upgrading in a controlled environment before upgrading the entire network.

2. Released Files

Releases Files The following is a list of files released for Aprisa XE software version 8.2.10.

File Name	File Type	File Function
README.txt	Information	Instructions relating to the software release
Rel_8_2_10_E0a.cfg	TFTP Upgrade	Type '8_2_10_E0a' for a 'full' TFTP upgrade for ETSI variants
Rel_8_2_10_E0.cfg	TFTP Upgrade	Type '8_2_10_E0' for a 'standard' TFTP upgrade for ETSI variants
Rel_8_2_10_E0h.cfg	TFTP Upgrade	Type '8_2_10_E0' for a 'standard' TFTP upgrade for HSD ETSI variants
Rel_8_2_10_E0p.cfg	TFTP Upgrade	Type '8_2_10_E0p' for a 'partial' TFTP upgrade for ETSI variants
Rel_8_2_10_F0a.cfg	TFTP Upgrade	Type '8_2_10_F0a' for a 'full' TFTP upgrade for FCC Part 90 variants
Rel_8_2_10_F0.cfg	TFTP Upgrade	Type '8_2_10_F0' for a 'standard' TFTP upgrade for FCC Part 90 variants
Rel_8_2_10_F0p.cfg	TFTP Upgrade	Type '8_2_10_F0p' for a 'partial' TFTP upgrade for FCC Part 90 variants
Rel_8_2_10_F1.cfg	TFTP Upgrade	Type '8_2_10_F1' for a 'standard' TFTP upgrade for FCC Part 101 variants
Rel_8_2_10_F1a.cfg	TFTP Upgrade	Type '8_2_10_F1a' for a 'full' TFTP upgrade for FCC Part 101 variants
Rel_8_2_10_F1p.cfg	TFTP Upgrade	Type '8_2_10_F1p' for a 'partial' TFTP upgrade for FCC Part 101 variants
F1_8_2_0.cfg	TFTP Upgrade	Used to load images for the newest DFXO and DFXS cards (rev D)
F2_8_2_0.cfg	TFTP Upgrade	Used to load images for all revisions of DFXO and DFXS cards
F3_8_2_0.cfg	TFTP Upgrade	Used to load images for the newest Modem card (rev D)
F_8_2_0.cfg	Control file	Used by Rel_8_2_10a.cfg during TFTP upgrade (FPGA firmware)
FH_8_2_0.cfg	Control file	Used by Rel_8_2_10h.cfg during TFTP upgrade (HSD)
M_7_3_0.cfg	Control file	Used by Rel_8_2_10_E0.cfg during TFTP upgrade (modem ETSI)
M_8_2_0.cfg	Control file	Used by Rel_8_2_10_E0h.cfg during TFTP upgrade (modem HSD ETSI)
M_7_3_4.cfg	Control file	Used by Rel_8_2_10_F0.cfg during TFTP upgrade (modem FCC part 90)
M_7_3_5.cfg	Control file	Used by Rel_8_2_10_F1.cfg during TFTP upgrade (modem FCC part 101)
O_8_1_4.cfg	Control file	Used by Rel_8_2_10.cfg during TFTP upgrade (FPGA firmware old)
P_8_1_4.cfg	Control file	Used by Rel_8_2_10p.cfg during TFTP upgrade (FPGA firmware partial)
R_8_1_4.cfg	Control file	Used by Rel_8_2_10a.cfg during TFTP upgrade process (RF synth files)
S_8_2_1.cfg	Control file	Used by Rel_8_2_10a.cfg during TFTP upgrade process (software)
X_8_2_1.cfg	Control file	Used by Rel_8_2_10a.cfg during TFTP upgrade process (SNMP)
C-fpga_E1-0-7-0.img	Firmware Image	Motherboard 1 rev C image file
C-fpga_E1-1-7-3.img	Firmware Image	Motherboard 1 rev D image file
C-fpga_E2-0-5-3.img	Firmware Image	Motherboard 2 rev C image file
C-fpga_E2-1-5-4.img	Firmware Image	Motherboard 2 rev D image file
C-fpga_E5-0-8-5.img	Firmware Image	QJET image file
C-fpga_E7-1-3-3.img	Firmware Image	Q4EM image file
C-fpga_E7-2-3-3.img	Firmware Image	Q4EM image file
C-fpga_E8-1-4-0.img	Firmware Image	DFXO image file
C-fpga_E8-2-4-0.img	Firmware Image	DFXO image file
C-fpga_E8-3-5-3.img	Firmware Image	DFXO image file
C-fpga_E9-0-4-1.img	Firmware Image	DFXS image file
C-fpga_E9-1-4-2.img	Firmware Image	DFXS image file
C-fpga_E9-2-4-1.img	Firmware Image	DFXS image file
C-fpga_EA-0-5-2.img	Firmware Image	Modem image file
C-fpga_EA-1-0-2.img	Firmware Image	Modem image file
C-fpga_EB-0-1-1.img	Firmware Image	QV24 image file
C-fpga_EC-0-1-4.img	Firmware Image	HSS image file
C-fpga_EC-1-1-7.img	Firmware Image	HSS image file
C-fpga_ED-0-1-0.img	Firmware Image	PSC image file
C-fpga_EE-0-1-0.img	Firmware Image	PIC image file
C-fpga_FA-1-1-0.img	Firmware Image	HSD Modem image file
C-CC-K-6_0_0.img	Kernel Image	Linux Kernel
C-CC-R-8_2_1.img	Software Image	Root File System

Released Files (cont)

File Name	File Type	File Function
C-crossconnect_8_2_1.cfg	Configuration	Cross Connect upgrade file
modem_7_3_0.cfg	Configuration	Modem Upgrade file (ETSI variants)
modem_7_3_4.cfg	Configuration	Modem Upgrade file (FCC part 90 variants)
modem_7_3_5.cfg	Configuration	Modem Upgrade file (FCC part 101 variants)
modem_8_2_0.cfg	Configuration	Modem Upgrade file (HSD ETSI variants)
compare_oids_8_2_1.cfg	Configuration	List of HSD common parameter OIDs
snmp_exclude_8_2_1.cfg	Configuration	Used by system for Aprisa Mux / Aprisa XE OID exclusion
modem_versions.txt	Readme file	List of modem versions vs RF variants
XE_300_400_synth.cfg	Configuration	Synthesizer Upgrade file for 300, 400 MHz frequency bands
XE_600_700_800_900_synth.cfg	Configuration	Synthesizer Upgrade file for 600, 700, 800, 900 MHz frequency bands
XE_1400_synth.cfg	Configuration	Synthesizer Upgrade file for 1400 MHz frequency band
XE_2000_2500_synth.cfg	Configuration	Synthesizer Upgrade file for 2000, 2500 MHz frequency bands
C-crossconnect_8_2_1.jar	Java Application	Cross Connect - used when running 7.1.4 or later
crossconnect_8_2_1.jar	Java Application	Cross Connect - used when running 7.0.6 or earlier
C-ccapp_exe_8_2_1.jar	Java Application	Cross Connect (stand alone application)
C-CC-B-7_1_1.srec	System	Bootloader for rev C motherboard (cannot be uploaded)
C-CC-B-8_1_4.srec	System	Bootloader for rev D motherboard (cannot be uploaded)
C-CC-F-8_2_1.img	System	Flash File System (cannot be uploaded)
C-swi_8_2_10_E0.swi	Inventory File	ETSI variants
C-swi_8_2_10_EA.swi	Inventory File	ETSI variants HSD
C-swi_8_2_10_F0.swi	Inventory File	FCC part 90 variants
C-swi_8_2_10_FA.swi	Inventory File	FCC part 90 variants HSD
C-swi_8_2_10_F1.swi	Inventory File	FCC part 101 variants
C-swi_8_2_10_FB.swi	Inventory File	FCC part 101 variants HSD
I_8_2_10_E0.cfg	Configuration	Inventory Configuration File (ETSI variants)
I_8_2_10_EA.cfg	Configuration	Inventory Configuration File (HSD ETSI variants)
I_8_2_10_F0.cfg	Configuration	Inventory Configuration File (FCC part 90 variants)
I_8_2_10_FA.cfg	Configuration	Inventory Configuration File (FCC part 90 variants HSD)
I_8_2_10_F1.cfg	Configuration	Inventory Configuration File (FCC part 101 variants)
I_8_2_10_FB.cfg	Configuration	Inventory Configuration File (FCC part 101 variants HSD)
4RF-APRISAXE-EVENTS.mib	SNMP MIB file	Aprisa XE Events MIB
4RF-MIB.mib	SNMP MIB file	Top level MIB
4RF-APRISAXE-MIB.mib	SNMP MIB file	Aprisa XE MIB
4RF-COMMON-MIB.mib	SNMP MIB file	Common MIB
4RF-PRODUCTS-MIB.mib	SNMP MIB file	Products MIB
4RF-APRISAXE-TC.mib	SNMP MIB file	Aprisa XE Textual Conventions MIB
4RF-COMMON-TC.mib	SNMP MIB file	Common Textual Conventions MIB

3. Upgrade Process

3.1. TFTP Upgrade

Software inventory file

Software release 7.5.03 and all future software releases, contain an inventory file (similar to a manifest file) which is used to validate the software files in the terminal. A special upgrade procedure is required to initiate the inventory file feature.

When upgrading terminals with software prior to 7.5.03:

1. Upgrade the Root File System with SuperVisor Local > Maintenance > Upload > Software by and browse to the file 'CC-R-8_2_10.img'. Click Upload.
2. Activate the 'CC-R-8_2_10.img' with SuperVisor Local > Maintenance > Image Table.
3. Reboot the terminal.
4. Perform the TFTP standard upgrade process.

Upgrade file usage

The following table defines the purpose of the upgrade version files:

Upgrade Version	Upgrade Type	Variant
8_2_10_E0a	Full TFTP upgrade	ETSI
8_2_10_E0	Standard TFTP upgrade	ETSI
8_2_10_E0h	Standard TFTP upgrade	ETSI HSD
8_2_10_E0p	Partial TFTP upgrade	ETSI
8_2_10_F0a	Full TFTP upgrade	FCC Part 90
8_2_10_F0	Standard TFTP upgrade	FCC Part 90
8_2_10_F0p	Partial TFTP upgrade	FCC Part 90
8_2_10_F0h	Standard TFTP upgrade	FCC Part 90 HSD
8_2_10_F1a	Full TFTP upgrade	FCC Part 101
8_2_10_F1	Standard TFTP upgrade	FCC Part 101
8_2_10_F1p	Partial TFTP upgrade	FCC Part 101
8_2_10_F1h	Standard TFTP upgrade	FCC Part 101 HSD

TFTP upgrade process

To run a TFTP upgrade process (example of ETSI upgrade):

Note: Make sure that the SuperVisor Local terminal is the near end terminal. The **Near** end terminal is the terminal that has its ethernet port physically connected to your IP network.

Run the TFTP program and set the 'Current Directory' to the root directory on the Aprisa CD.

Select the SuperVisor menu item Remote > Maintenance > Upload > TFTP Upgrade

Type the IP address of the TFTP server in the **TFTP Server** field.

Type the version number in the **Upgrade Version** field e.g. '8_2_10_E0'.

Click the Apply button and wait for the upgrade process to complete and report 'success'.

Reboot the remote terminal.

Select the SuperVisor menu item Local > Maintenance > Upload > TFTP Upgrade

Type the IP address of the TFTP server in the **TFTP Server** field.

Type the version number in the **Upgrade Version** field e.g. '8_2_10_E0'.

Click the Apply button and wait for the upgrade process to complete and report 'success'.

Reboot the local terminal.

TFTP upgrade process types

Aprisa XE terminals running the older Bootloader software have a limitation on the number of software images that can be loaded simultaneously into a terminal.

First, determine which Bootloader version your terminal is running by using the SuperVisor menu item Maintenance > Support Summary and look for the 'Bootloader Version' number.

(1) If your terminal is running Bootloader version 1, use the TFTP full upgrade process.

(2) If your terminal is running Bootloader version 0 and running a software version prior to 7.0.6, use the TFTP partial upgrade process.

(3) If your terminal is running Bootloader version 0 and running a software version 7.0.6 or later, use the TFTP standard upgrade process.

TFTP partial upgrade process

Run the TFTP upgrade process by typing 8_2_10_E0p in the Upgrade Version field.

This will perform a partial upgrade which will delete unnecessary image files that might be taking up space in the Image Table (which could prevent a normal upgrade).

Reboot the terminal.

Run a TFTP standard upgrade process on the terminal.

Reboot the terminal again.

TFTP standard upgrade process

This TFTP standard upgrade process excludes FPGA images for the newly introduced revisions of the Modem, DFXO and DFXS cards.

Run the TFTP upgrade process by typing '8_2_10_E0' in the Upgrade Version field.

If the standard upgrade fails, it may be necessary to make space for the new images by manually deleting 'Inactive' firmware image files.

To delete a firmware image file, select the SuperVisor menu item Maintenance > Image Table, select the firmware image and click on Edit. Set the IMAGE DETAILS Command to 'Delete' and click 'Apply'.

Reboot the terminal.

Additional TFTP upgrade options have been provided to load the new images separately. Run the TFTP upgrade process using the file:

- 'F1_8_2_0' to load images for the newest DFXO and DFXS cards (rev D).
- 'F2_8_2_0' to load images for all revisions of DFXO and DFXS cards.
- 'F3_8_2_0' to load images for the newest Modem card (rev D).

Reboot the terminal again.

TFTP full upgrade process

Run the TFTP upgrade process by typing '8_2_10_E0a' in the Upgrade Version field.

Reboot the terminal.

4. Major Changes

Major enhancements

Hitless Space Diversity (HSD) link sensitivity has been improved by 4 dB.

Major bug fixes

Intermittent cross connection failure of a framed E1 port

Minor bug fixes

None

5. System Software

5.1. System Software Changes

Improved HSD link sensitivity

Hitless Space Diversity (HSD) link sensitivity has been improved by 4 dB.

5.2. System Software Bug Fixes

Framed E1 bug

Intermittently, entering a framed E1 cross connect fails to establish a cross connection. This was found to be caused by the order of allocation of TDM bus capacity. This bug has been corrected in software version 8.2.10.

Fan alarm bug

On rev D motherboards, a fan 2 failure does not generate an alarm. This bug has been corrected in software version 8.2.10.

6. SuperVisor

6.1. SuperVisor Enhancements

None

6.2. SuperVisor Bug Fixes

None

7. Cross connections application

7.1. Cross Connections Application Enhancements

None

7.2. Cross Connections Application Bug Fixes

None

7.3. Setup Menu Changes

None

8. Recommendations

Java 1.6 JRE

That all PCs running the Aprisa XE support software, SuperVisor and the Cross Connections application be upgraded to Java 1.6 JRE (JVM).
